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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number

Filing Date

7 July 2003

First Named Inventor

	Roy Alan Hastings
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Art Unit

Examiner Name

Attorney Docket Number

TI-35919

Sheet

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U. S. PATENT DOCUMENTS

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Sheet 2	of 2	Attorney Docket Number	TI-35919

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		HASTINGS, The ART of ANALOG LAYOUT, Prentice Hall, 2001, pp. 318-322	
		GILBERT, "Current-mode Circuits From A Translinear Viewpoint: A Tutorial", in Lidgley et al., Analogue IC design: the current-mode approach, 1999, p. 19	
		HASTINGS, "Integrated MOSFET Interface for a Synchronously-Rectified SMPS, Proc. of the 1995 Bipolar/BiCMOS Circuits and Technology Meeting, 1995, pp. 58-61	

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